

1/6

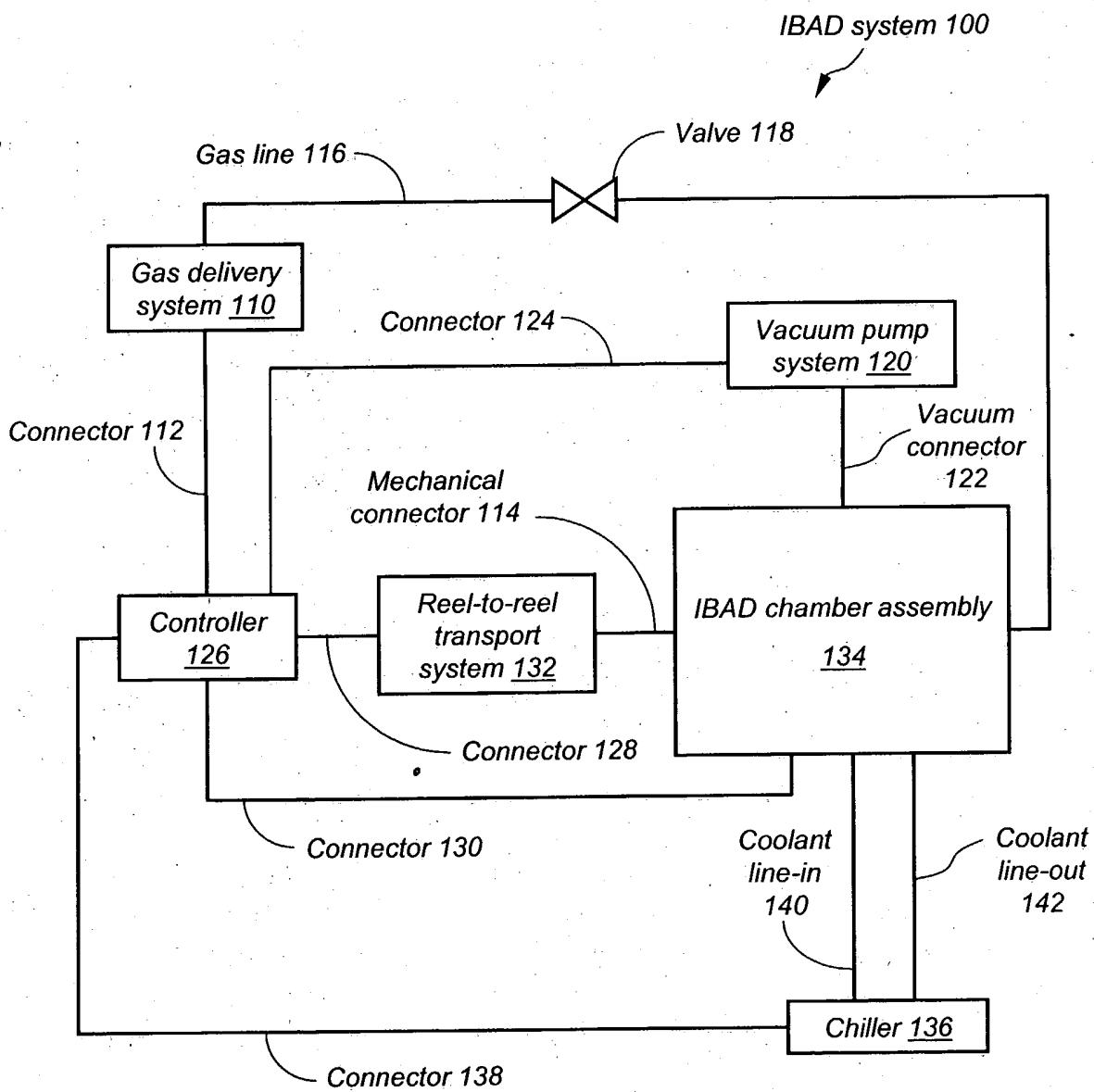


FIG. 1

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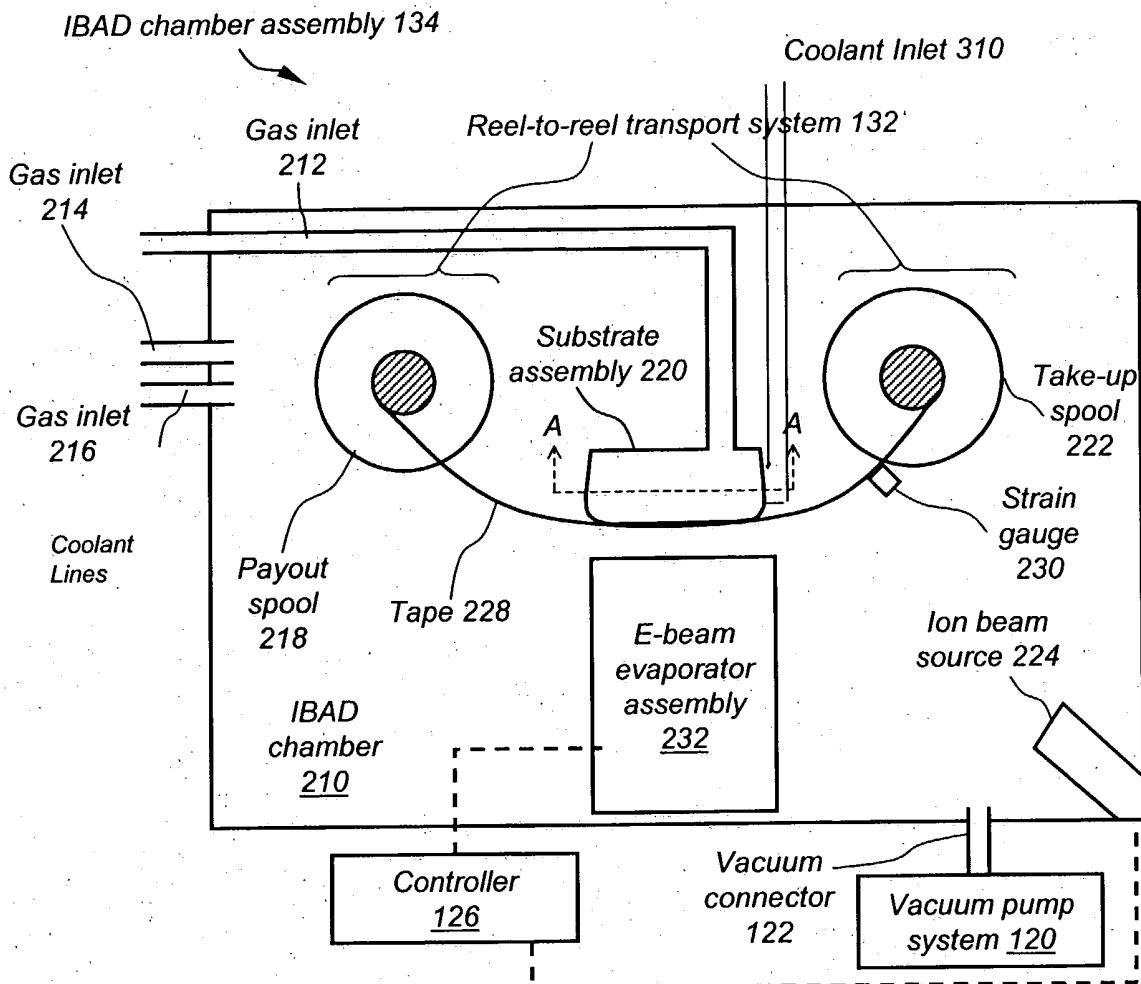


FIG. 2

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Substrate assembly 220

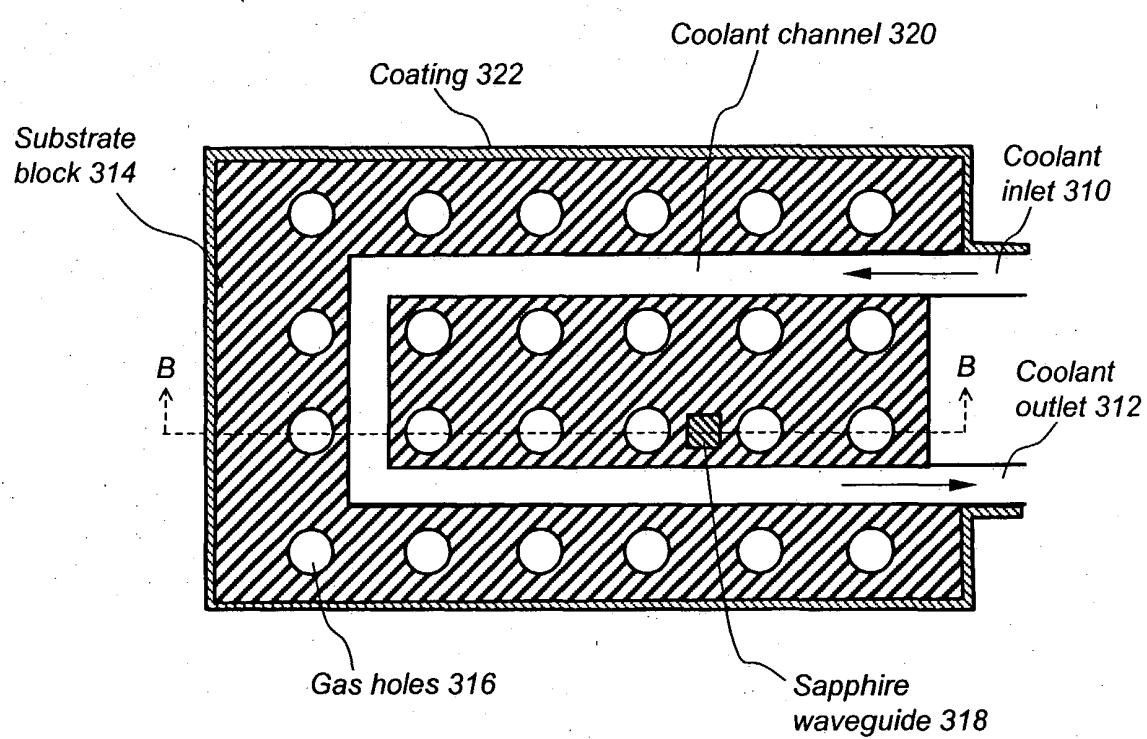


FIG. 3A

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Substrate assembly 220

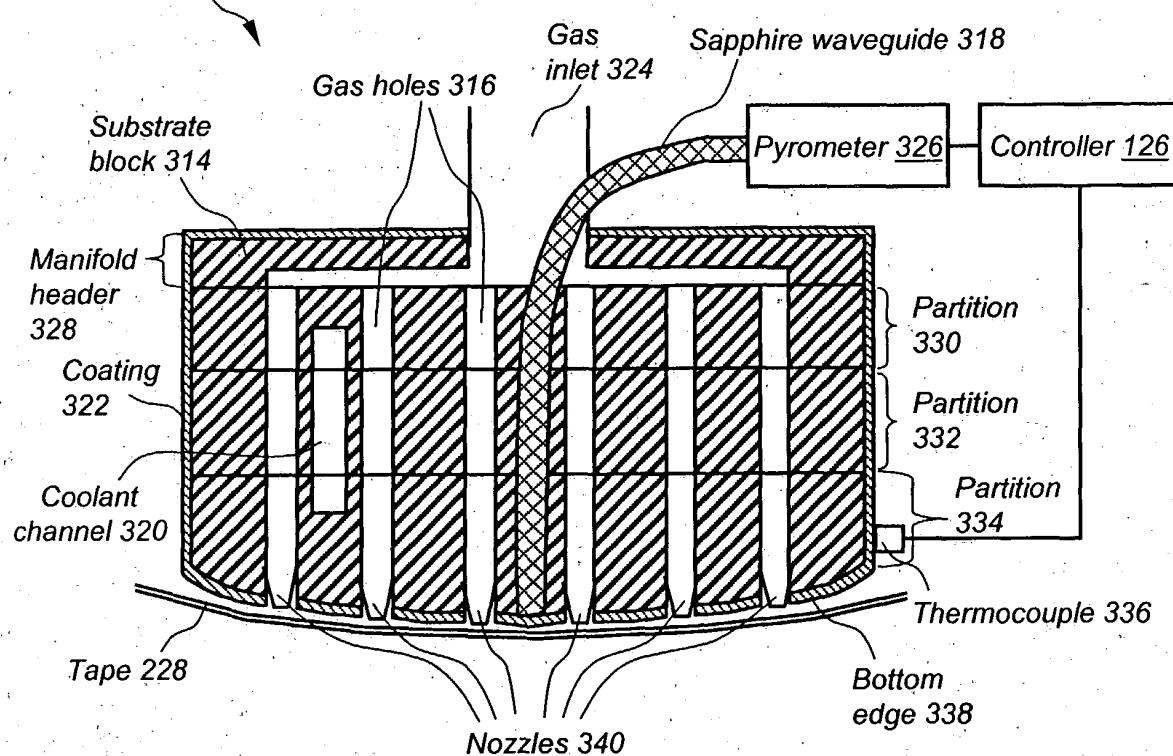


FIG. 3B

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Substrate assembly 220

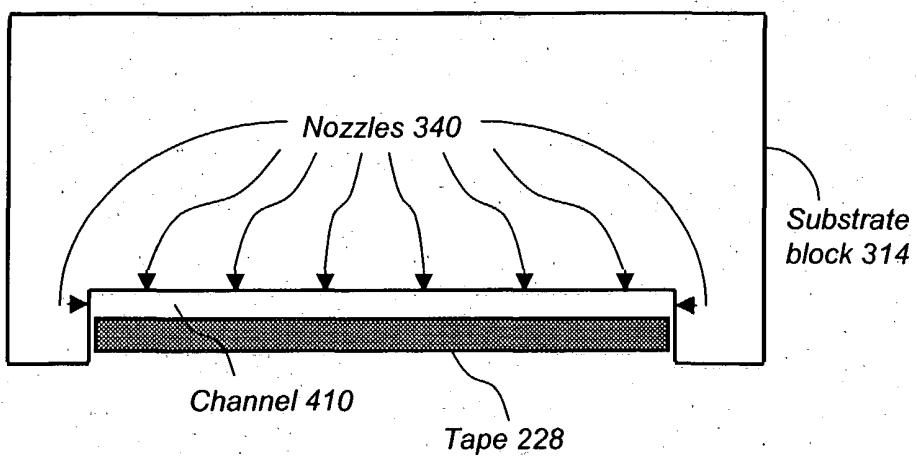


FIG. 4A

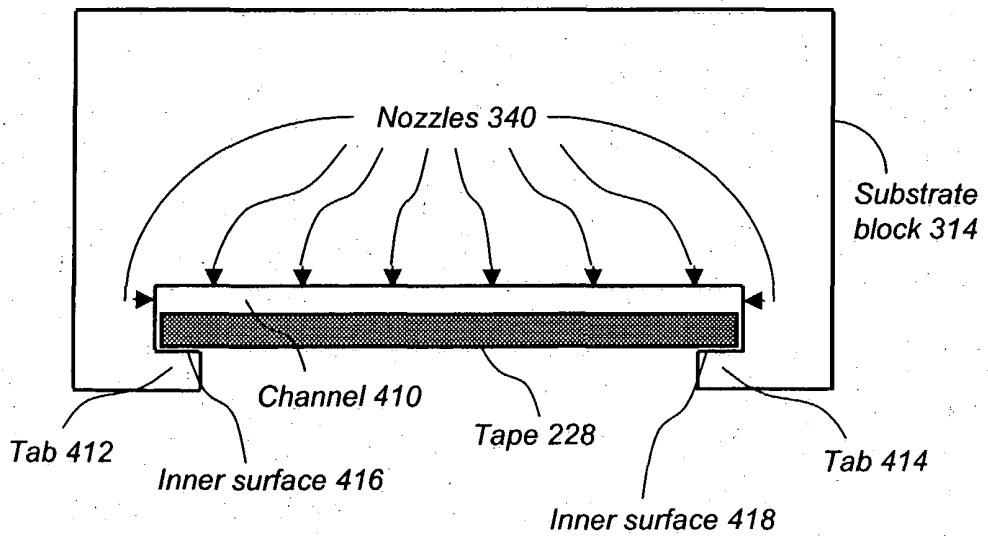


FIG. 4B

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Method 500

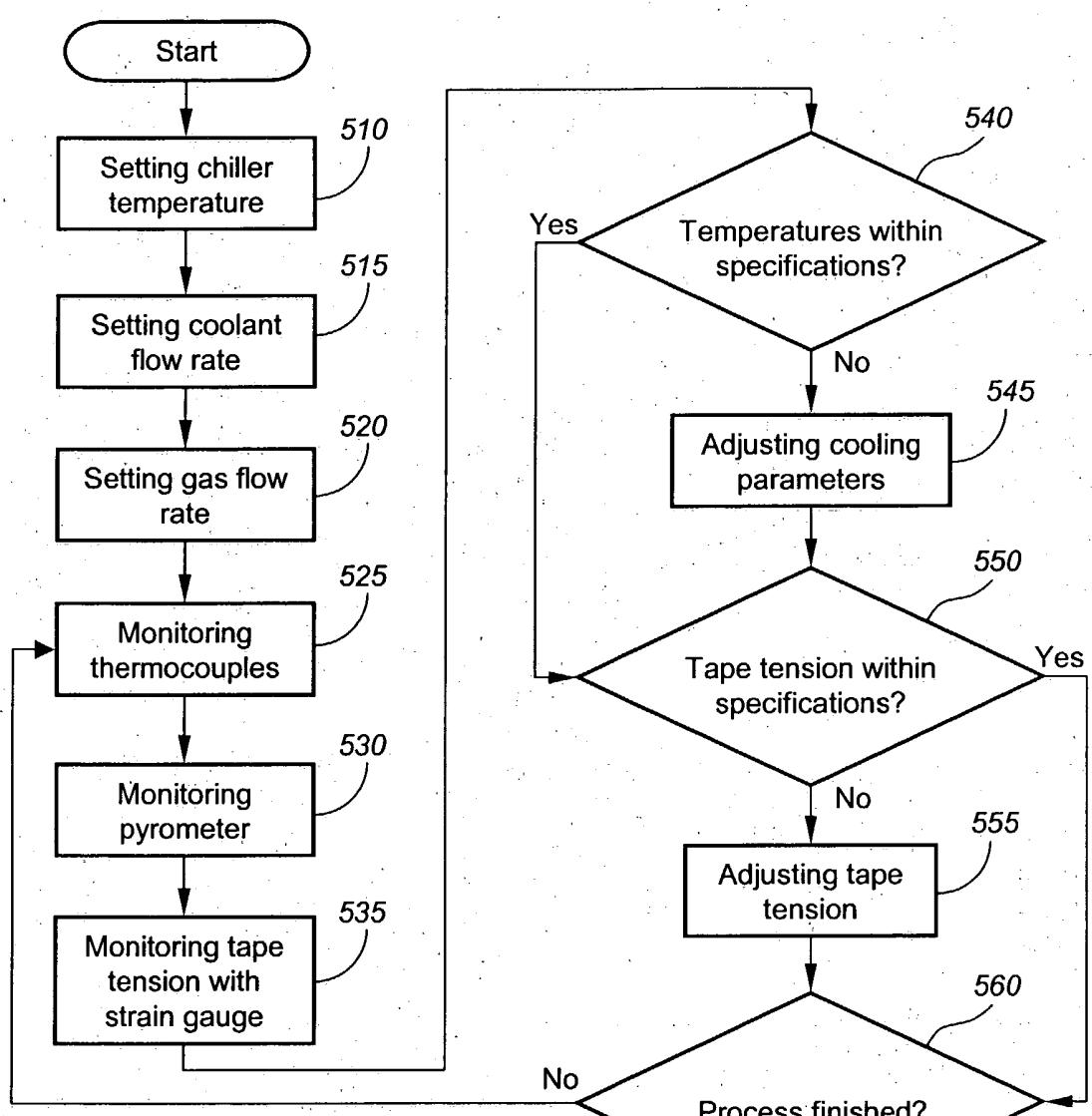


FIG. 5

0111 65 0000 0000 0000 0000 0000 0000

0000 0000 0000 0000 0000 0000 0000 0000

V8